
Etching of Semiconductor Devices

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Plasma etching or Reactive Ion Etching (RIE) is the workhorse for patterning of semiconductor devices since the early 1980s when it replaced wet etching in manufacturing. Today, RIE is reaching levels of performance which were unimaginable back then. At the same time, etching technologies such as Atomic Layer Etching (ALE), radical, dry vapor and Ion Beam Etching are finding their way into manufacturing for certain applications.

In this review, we present an overview of dry etching technologies used in semiconductor manufacturing. The emphasis is on the elementary surface processes and how they impact the performance on the wafer. We will start from less complex etching technologies which use just one kind of etching species, such as neutrals, radicals or ions. Then we combine these techniques into cycling processes which leads to the discussion of ALE.

ALE transitioned to manufacturing because of unique performance benefits which come from self-saturation of the steps and the use of very low ion energies or just thermal means to remove material. The former property gives uniformity across all length scales on the wafer. Low ARDE and surface smoothness are the most attractive features. Low level removal energies deliver selectivity which is a key requirement for etching with atomic scale fidelity.

The highest level of complexity is reached in RIE with simultaneous species fluxes. Reactor designs for the various etching technologies and process control will be covered. Finally, an outlook into the future of semiconductor device etching will be given.

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