Modeling of dry cleaning processes of semiconductor surfaces using fluorine-based etchant gases

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Introduction of 3D device architectures and scaling of device dimensions of integrated circuits inflict major challenges in modern semiconductor manufacturing technology. Formation of functional structures down to sub-10nm scale requires extremely smooth and clean surfaces. Utilization of the conventional wet cleaning processes has its limitation at this projected structural feature. Dry cleaning of semiconductor surfaces is considered to be a compelling technique as it demonstrates the multifaceted advantages over wet etching processes, such as the ability to access smaller structures and the absence of damage caused by surface tension. Fluorine-based molecules have been used for dry vapor phase etching of semiconductor surfaces and many experimental studies with varying process parameters have been conducted extensively. The underlying chemistry between etchants and surfaces are complex and requires profound exploration in the theoretical ground. Ab initio atomic scale simulation can be a convenient mean to interpret well established experimental techniques along with outlining new etching process routes. A detailed simulation study of reaction mechanism between etchants gases with various surfaces is yet inadequate in the literature.

In this study, density functional theory (DFT) calculations have been employed to investigate the effect of etching gases on different surfaces. The reaction in which the H-terminated Si or Ge surface or the OH-terminated SiO2 or GeO2 surface is fluorinated by a fluorine-based gas such as NF3, CIF3, CF4 or CHF3 is modeled and simulated by the DFT method. Surface reaction mechanisms were studied by the reaction energy and the energy barrier. A comprehensive understanding of the surface reactions during the dry cleaning process can provide valuable insights into the selective epitaxial growth process.

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