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31D. Ferroelectric Technology for Logic and Memories	
Session Date:	March 9(Thu.), 2023
Session Time:	13:00-14:35
Session Room:	Room D (#308 bc)
Session Chair:	Prof. Gong Xiao (National University of Singapore)
	Dr. Kyung Chul Jang (SK Hynix)

[31D-1] [Keynote] 13:00-13:25

Integrated, Ultra Thin Ferroelectric Materials for Logic and Memory

Sayeef Salahuddin

University of California

[31D-2] [Invited] 13:25-13:50

Ferroelectric Field-Effect Transistors: Logic Compatibility and Microstructure

Asif Isla Khan

Georgia Institute of Technology

[31D-3] 13:50-14:05

A Comprehensive Simulation Study on Capacitive Memory with Metal-Ferroelectric-Insulator-Semiconductor Structure

Hongrui Zhang¹, Chengji Jin¹, Xiaole Jia², Jiajia Chen¹, Huan Liu¹, Yan Liu², Xiao Yu¹ and Genquan Han²

¹Zhejiang Lab, ²Xidian University

[31D-4] 14:05-14:20

Ferroelectric-like Behaviors of Mobile-Ionic Field-Effect Transistors with Amorphous Dielectrics

Huan Liu¹, Chengji Jin¹, Jiajia Chen¹, Xiao Yu¹, Jing Li², Yue Peng², Ran Cheng³, Bing Chen³, Yan Liu², Yue Hao² and Genquan Han²

¹Zhejiang Lab, ²Xidian University, ³Zhejiang University

[31D-5] 14:20-14:35

Self-Heating and Interface Traps Assisted Early Aging Revelation and Reliability Analysis of Negative Capacitance FinFET

Rajeewa Kumar Jaisawal¹, Sunil Rathore¹, Navneet Gandhi¹, P. N. Kondekar¹, Shashank Banchhor², V. Bharath Sreenivasulu³, Young Suh Song⁴ and Navjeet Bagga⁵

¹Pandit Dwarka Prasad Mishra Indian Institute of Information Technology, Design and Manufacturing Jabalpur, ²Indian Institute of Technology Roorkee, ³Vellore Institute of Technology, ⁴Korea Military Academy, ⁵Indian Institute of Technology Bhubaneswar